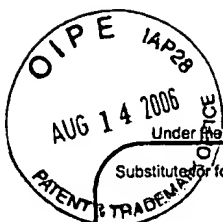


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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

### Complete if Known

Application Number	09/667,164
Filing Date	September 21, 2000
First Named Inventor	William E. Ballachino
Art Unit	2124
Examiner Name	Chat C. Do
Attorney Docket Number	00-C-050

Sheet	2	of	2
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### NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
CD	BA	Department of Electrical Engineering of Linkopings University, "Arithmetic Building Blocks", Prentice Hall, 1995, pp. 1-21, retrieved from the internet: <a href="http://www.ek.isy.liu.se/course/tsek30/current/Arithm.pdf">www.ek.isy.liu.se/course/tsek30/current/Arithm.pdf</a> , on June 1, 2006.	
CD	BB	David A. Patterson et al., "Computer Architecture A Quantitative Approach", 1990 Morgan Kaufmann Publishers, Inc., San Francisco, California, pp. A45-A46.	

Examiner Signature	/Chat Do/ (09/21/2006)	Date Considered	09/21/2006
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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